



Inventor: John T. Moore

Title: Methods of Forming Oxide Regions Over Semiconductor Substrates

Assignee: Micron Technology, Inc.

#14
IDS
J. Lincoln
8/20/02

INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: August 6, 2002 Attorney: Jennifer J. Taylor
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